

EAST SEARCH

8/23/03

| L# | Hits | Search String | Databases |
|----------------------------------|------|--|---|
| L1 | 274 | simulator\$2 and (ipc or interprocess or "inter-process") | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| L2 | 69 | 1 and diagnostic\$5 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| L3 | 3529 | logic and verific\$6 and diagnostic | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| L4 | 251 | 3 and wrapper\$2 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| L5 | 150 | 3 and (logic near4 verific\$6) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| L6 | 6 | 5 and wrapper\$2 | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB |
| Results of search set L1: | | | |
| US 6598222 B2 | | Programming method for concurrent programs and program supporting apparatus thereof | 20030722 717/154 |
| US 6597727 B2 | | Programmable modem apparatus for transmitting and receiving digital data, design method and use method for the modem | 20030722 375/147 |
| US 6587995 B1 | | Enhanced programmable core model with integrated graphical debugging functionality | 20030701 716/4 |
| US 6553485 B2 | | Non-stalling circular counterflow pipeline processor with reorder buffer | 20030422 712/219 |
| US 6549881 B1 | | Interface for interfacing simulation tests written in a high-level programming language to a simulation model | 20030415 703/21 |
| US 6542739 B1 | | Priority and preemption service system for satellite related communication using central controller | 20030401 455/427 |
| US 6539522 B1 | | Method of developing re-usable software for efficient verification of system-on-chip integrated circuit designs | 20030325 716/5 |
| US 6535903 B2 | | Method and apparatus for maintaining translated routine stack in a binary translation environment | 20030318 709/100 |
| US 6529731 B2 | | Network control center for satellite communication system | 20030304 455/427 |
| US 6522635 B1 | | Communication protocol for satellite data processing | 20030218 370/314 |
| US 6510541 B1 | | Database having a hierarchical structure utilized for designing system-on-chip integrated circuit devices and a method of designing the same | 20030121 716/5 |
| US 6507809 B1 | | Method and system for simulating performance of a computer system | 20030114 703/21 |
| US 6502237 B1 | | Method and apparatus for performing binary translation method and apparatus for performing binary translation | 20021231 717/136 |

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|---------------|--|----------|-----------|
| US 6480954 B2 | Method of time multiplexing a programmable logic device | 20021112 | 713/1 |
| US 6473824 B1 | Dynamic association of input/output device with application programs | 20021029 | 348/222.1 |
| US 6433795 B1 | System for integrating an on-line service community with a foreign service | 20020813 | 345/738 |
| US 6427224 B1 | Method for efficient verification of system-on-chip integrated circuit designs including an embedded processor | 20020730 | 716/4 |
| US 6421823 B1 | Bidirectional socket stimulus interface for a logic simulator | 20020716 | 717/135 |
| US 6421251 B1 | Array board interconnect system and method | 20020716 | 361/788 |
| US 6411806 B1 | Virtual network configuration and management system for satellite communications system | 20020625 | 455/428 |
| US 6408381 B1 | Mechanism for fast access to control space in a pipeline processor | 20020618 | 712/225 |
| US 6389383 B1 | System and method for interfacing software and hardware | 20020514 | 703/21 |
| US 6389379 B1 | Converfication system and method | 20020514 | 703/14 |
| US 6385765 B1 | Specification and verification for concurrent systems with graphical and textual editors | 20020507 | 717/100 |
| US 6380479 B2 | Photovoltaic element and method for manufacture thereof | 20020430 | 136/246 |
| US 6370492 B1 | Modified design representation for fast fault simulation of an integrated circuit | 20020409 | 703/13 |
| US 6351805 B1 | Non-stalling circular counterflow pipeline processor with reorder buffer | 20020226 | 712/219 |
| US 6351803 B1 | Mechanism for power efficient processing in a pipeline processor | 20020226 | 712/216 |
| US 6339836 B1 | Automated design partitioning | 20020115 | 716/5 |
| US 6338088 B1 | Service creation apparatus for a communications network | 20020108 | 709/226 |
| US 6321366 B1 | Timing-insensitive glitch-free logic system and method | 20011120 | 716/6 |
| US 6307877 B1 | Programmable modem apparatus for transmitting and receiving digital data, design method and use method for the modem | 20011023 | 375/130 |
| US 6304915 B1 | System, method and article of manufacture for a gateway system architecture with system administration information accessible from a browser | 20011016 | 709/250 |
| US 6298039 B1 | High availability distributed call processing method and apparatus | 20011002 | 370/216 |
| US 6282614 B1 | Apparatus and method for reducing the power consumption of a microprocessor with multiple levels of caches | 20010828 | 711/122 |
| US 6279054 B1 | Arbitrator for multiple processes sharing a port | 20010821 | 710/36 |
| US 6275980 B1 | Programming method for concurrent programs and program supporting apparatus thereof | 20010814 | 717/124 |
| US 6272341 B1 | Network engineering/systems engineering system for mobile satellite communication system | 20010807 | 455/428 |
| US 6272338 B1 | Network control center for satellite communication system | 20010807 | 455/430 |
| US 6269396 B1 | Method and platform for interfacing between application programs performing telecommunications functions and an operating system | 20010731 | 709/223 |
| US 6263430 B1 | Method of time multiplexing a programmable logic device | 20010717 | 713/1 |
| US 6247115 B1 | Non-stalling circular counterflow pipeline processor with reorder buffer | 20010612 | 712/219 |

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| US 6243580 B1 | Priority and preemption service system for satellite related communication using central controller | 20010605 | 455/428 |
| US 6240502 B1 | Apparatus for dynamically reconfiguring a processor | 20010529 | 712/15 |
| US 6226789 B1 | Method and apparatus for data flow analysis | 20010501 | 717/138 |
| US 6219778 B1 | Apparatus for generating out-of-order results and out-of-order condition codes in a processor | 20010417 | 712/23 |
| US 6212566 B1 | Interprocess communication protocol system modem | 20010403 | 709/230 |
| US 6208954 B1 | Method for scheduling event sequences | 20010327 | 703/16 |
| US 6208954 B | Simulator process scheduling method involves updating outputs of simulators until current simulation time equals target time point | 20010327 | 136/246 |
| US 6207890 B1 | Photovoltaic element and method for manufacture thereof | 20010327 | |
| US 6199095 B1 | System and method for achieving object method transparency in a multi-code execution environment | 20010306 | 709/107 |
| US 6185409 B1 | Network engineering/systems engineering system for mobile satellite communication system | 20010206 | 455/12.1 |
| US 6178394 B1 | Protocol checking for concurrent systems | 20010123 | 703/17 |
| US 6167363 A | Design for a simulation module using an object-oriented programming language | 20001226 | 703/14 |
| US 6167363 A | Comparison of model simulation output signal of logic circuit, involves starting RTL and HDL model simulation in two processes, respectively of operating system supporting interprocess communication to run processes | 20001226 | 712/219 |
| US 6163839 A | Non-stalling circular counterflow pipeline processor with reorder buffer | 20001219 | 434/308 |
| US 6160986 A | Interactive toy | 20001212 | 707/1 |
| US 6151597 A | Host DBMS simulator and simulation method thereof | 20001121 | 703/13 |
| US 6151567 A | Data communication analysis and simulation tool | 20001121 | |
| US 6151567 A | Data communication system simulator for aircraft communication, includes communication protocol interconnect module coupled to programmed simulator to enable communication using specific communication protocol | 20001121 | |
| US 6144982 A | Pipeline processor and computing system including an apparatus for tracking pipeline resources | 20001107 | 709/104 |
| US 6134516 A | Simulation server system and method | 20001017 | 703/27 |
| US 6131081 A | System and method for digital simulation of an electrical circuit | 20001010 | 703/16 |
| US 6112085 A | Virtual network configuration and management system for satellite communication system | 20000829 | 455/428 |
| US 6110217 A | System and method for synchronization of multiple analog servers on a simulation backplane | 20000829 | 703/14 |
| US 6110217 A | Multitask computer system for simulation of electronic circuit, has interface module which selects back tracking or non-back tracking simulators to simulate divided circuit blocks within set time | 20000829 | |
| US 6108494 A | Optimizing runtime communication processing between simulators | 20000822 | 703/14 |

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|--------------|---|----------|---------|
| US 6108309 A | SONET network element simulator | 20000822 | 370/241 |
| US 6106298 A | Reconfigurable easily deployable simulator | 20000822 | 434/29 |
| US 6091897 A | Fast translation and execution of a computer program on a non-native architecture by use of background translator | 20000718 | 717/138 |
| US 6085305 A | Apparatus for precise architectural update in an out-of-order processor | 20000704 | 712/23 |
| US 6074427 A | Apparatus and method for simulating multiple nodes on a single machine | 20000613 | 703/21 |
| US 6058466 A | System for allocation of execution resources amongst multiple executing processes | 20000502 | 712/15 |
| US 6058307 A | Priority and preemption service system for satellite related communication using central controller | 20000502 | 455/428 |
| US 6035374 A | Method of executing coded instructions in a multiprocessor having shared execution resources including active, nap, and sleep states in accordance with cache miss latency | 20000307 | 711/118 |
| US 6026230 A | Memory simulation system and method | 20000215 | 703/13 |
| US 6020884 A | System integrating an on-line service community with a foreign service | 20000201 | 345/747 |
| | Method for tracking and displaying user's spatial position and orientation, a method for representing virtual reality for a user, and systems of embodiment of such methods | 19991221 | 345/156 |
| US 6005548 A | Protein-containing polymer composition for oral administration | 19991221 | 424/486 |
| US 6004583 A | Means and apparatus for maintaining condition codes in an unevaluated state | 19991207 | 712/226 |
| US 6000028 A | Compiler-oriented apparatus for parallel compilation, simulation and execution of computer programs and hardware models | 19991207 | 717/149 |
| US 5999734 A | Simulation tool for a networking code | 19991130 | 703/21 |
| US 5995741 A | Expert manufacturing system | 19991123 | 703/6 |
| US 5991528 A | System, method and article of manufacture for a payment gateway system architecture for processing encrypted payment transactions utilizing a multichannel, extensible, flexible architecture | 19991102 | 709/217 |
| US 5978840 A | Method of time multiplexing a programmable logic device | 19991102 | 365/182 |
| US 5978260 A | Programmable logic device including configuration data or user data memory slices | 19990928 | 365/182 |
| US 5959881 A | Method for precise architectural update in an out-of-order processor | 19990928 | 712/237 |
| US 5958047 A | Simulation of computer-based telecommunications system | 19990831 | 703/13 |
| US 5946474 A | Arbitrator for modem in a system without shared process space that passes a duplicate handle to a first listening application with matching identification string | 19990817 | 710/36 |
| US 5938745 A | System, method and article of manufacture for a gateway system architecture with system administration information accessible from a browser | 19990803 | 709/250 |
| US 5931917 A | Method and apparatus for performing binary translation | 19990727 | 717/159 |
| US 5930509 A | Integrated circuit test stimulus verification and vector extraction system | 19990706 | 716/2 |

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|--------------|--|----------|------------|
| US 5915115 A | Control system and method for direct execution of software application information models without code generation | 19990622 | 717/155 |
| US 5913164 A | Conversion system used in billing system for mobile satellite system | 19990615 | 455/427 |
| US 5907607 A | Service creation apparatus for a communications network | 19990525 | 379/201.03 |
| US 5907494 A | Computer system and method for performing design automation in a distributed computing environment | 19990525 | 703/1 |
| US 5893123 A | System and method of integrating a spreadsheet and external program having output data calculated automatically in response to input data from the spreadsheet | 19990406 | 715/504 |
| US 5890181 A | System and method for remotely grouping contents of an action history stack | 19990330 | 715/530 |
| US 5890008 A | Method for dynamically reconfiguring a processor | 19990330 | 712/15 |
| US 5889954 A | Network manager providing advanced interconnection capability | 19990330 | 709/223 |
| US 5883639 A | Visual software engineering system and method for developing visual prototypes and for connecting user code to them | 19990316 | 345/473 |
| US 5872810 A | Programmable modem apparatus for transmitting and receiving digital data, design method and use method for said modem | 19990216 | 375/222 |
| US 5870588 A | Design environment and a design method for hardware/software co-design | 19990209 | 703/13 |
| US 5870585 A | Design for a simulation module using an object-oriented programming language | 19990209 | 703/15 |
| US 5867723 A | Advanced massively parallel computer with a secondary storage device coupled through a secondary storage interface | 19990202 | 712/11 |
| US 5862391 A | Power management control system | 19990119 | 713/300 |
| US 5860018 A | Method for tracking pipeline resources in a superscalar processor | 19990112 | 712/23 |
| US 5860009 A | Programming method for concurrent programs and program supporting apparatus thereof | 19990112 | 717/149 |
| US 5857091 A | Machine and method for simulating a processor-based digital system | 19990105 | 703/21 |
| US 5856933 A | System and method for digital simulation of an electrical circuit | 19990105 | 703/16 |
| US 5850225 A | Image mapping system and process using panel shear transforms | 19981215 | 345/427 |
| US 5842125 A | Network control center for satellite communication system | 19981124 | 455/426.1 |
| US 5842017 A | Method and apparatus for forming a translation unit | 19981124 | 717/158 |
| US 5838988 A | Computer product for precise architectural update in an out-of-order processor | 19981117 | 712/32 |
| US 5838954 A | Computer-implemented method of optimizing a time multiplexed programmable logic device | 19981117 | 716/16 |
| US 5838947 A | Modeling, characterization and simulation of integrated circuit power behavior | 19981117 | 703/14 |
| US 5825662 A | Computer-implemented method of optimizing a time multiplexed programmable logic device | 19981020 | 716/3 |
| US 5819270 A | Computer system for displaying representations of processes | 19981006 | 707/7 |

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|--------------|---|----------|----------|
| US 5815688 A | Verification of accesses in a functional model of a speculative out-of-order computer system | 19980929 | 716/4 |
| US 5812394 A | Object-oriented computer program, system, and method for developing control schemes for facilities | 19980922 | 700/17 |
| US 5805470 A | Verification of instruction and data fetch resources in a functional model of a speculative out-of order computer system | 19980908 | 703/27 |
| US 5802373 A | Method for providing a pipeline interpreter for a variable length instruction set | 19980901 | 717/139 |
| US 5801938 A | Data processing method and apparatus for parallel discrete event simulation | 19980901 | 700/2 |
| US 5799266 A | Automatic generation of test drivers | 19980825 | 702/123 |
| US 5796393 A | System for intergrating an on-line service community with a foreign service | 19980818 | 345/733 |
| US 5794012 A | Verification of strongly ordered memory accesses in a functional model of an out-of-order computer system | 19980811 | 703/21 |
| US 5784313 A | Programmable logic device including configuration data or user data memory slices | 19980721 | 365/182 |
| US 5774695 A | Protocol interface gateway and method of connecting an emulator to a network | 19980630 | 703/26 |
| US 5768148 A | Man machine interface for power management control systems | 19980616 | 700/286 |
| US 5765011 A | Parallel processing system having a synchronous SIMD processing with processing elements emulating SIMD operation using individual instruction streams | 19980609 | 712/20 |
| US 5764155 A | Dynamic data exchange server | 19980609 | 700/295 |
| US 5761483 A | Optimizing and operating a time multiplexed programmable logic device | 19980602 | 716/2 |
| US 5754871 A | Parallel processing system having asynchronous SIMD processing | 19980519 | 712/20 |
| US 5752068 A | Mesh parallel computer architecture apparatus and associated methods | 19980512 | 712/16 |
| US 5752067 A | Fully scalable parallel processing system having asynchronous SIMD processing | 19980512 | 712/16 |
| US 5740469 A | Apparatus for dynamically reading/writing multiple object file formats through use of object code readers/writers interfacing with generalized object file format interface and applications programmers' interface | 19980414 | 710/65 |
| US 5732247 A | Interface for interfacing simulation tests written in a high-level programming language to a simulation model | 19980324 | 703/13 |
| US 5732213 A | System and method of testing open systems interconnection (OSI) layers in telecommunication networks | 19980324 | 709/224 |
| US 5713075 A | Network engineering/systems engineering system for mobile satellite communication system | 19980127 | 455/427 |
| US 5712964 A | Computer graphics data display device and method based on a high-speed generation of a changed image | 19980127 | 345/418 |
| US 5704200 A | Agricultural harvester ground tracking control system and method using fuzzy logic | 19980106 | 56/10.2E |

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| US 5701441 A | Computer-implemented method of optimizing a design in a time multiplexed programmable logic device | 19971223 | 716/16 |
| US 5694325 A | Semiconductor production system | 19971202 | 700/121 |
| US 5692907 A | Interactive cardiac rhythm simulator | 19971202 | 434/262 |
| US 5655084 A | Radiological image interpretation apparatus and method | 19970805 | 705/3 |
| US 5646545 A | Time multiplexed programmable logic device | 19970708 | 326/38 |
| US 5644688 A | Boolean trajectory solid surface movement method | 19970701 | 345/420 |
| US 5629637 A | Method of time multiplexing a programmable logic device | 19970513 | 326/93 |
| US 5627886 A | System and method for detecting fraudulent network usage patterns using real-time network monitoring | 19970506 | 379/111 |
| US 5619885 A | Run-time dynamically adaptive computer process for facilitating communication between computer programs | 19970408 | 703/20 |
| US 5610833 A | Computer-aided design methods and apparatus for multilevel interconnect technologies | 19970311 | 716/11 |
| US 5600579 A | Hardware simulation and design verification system and method | 19970204 | 703/13 |
| US 5600263 A | Configuration modes for a time multiplexed programmable logic device | 19970204 | 326/39 |
| US 5590356 A | Mesh parallel computer architecture apparatus and associated methods | 19961231 | 712/31 |
| US 5586230 A | Surface sweeping method for surface movement in three dimensional topography simulation | 19961217 | 345/420 |
| US 5583450 A | Sequencer for a time multiplexed programmable logic device | 19961210 | 326/41 |
| US 5581778 A | Advanced massively parallel computer using a field of the instruction to selectively enable the profiling counter to increase its value in response to the system clock | 19961203 | 712/16 |
| US 5579527 A | Apparatus for alternately activating a multiplier and a match unit | 19961126 | 712/16 |
| US 5546562 A | Method and apparatus to emulate VLSI circuits within a logic simulator | 19960813 | 703/14 |
| US 5544318 A | Asynchronous media server request processing system for servicing reprioritizing request from a client determines whether or not to delay executing said reprioritizing request | 19960806 | 709/207 |
| US 5544316 A | Method and system for optionally registering a local process to allow participation in a single system semantic | 19960806 | 709/310 |
| US 5519618 A | Airport surface safety logic | 19960521 | 701/120 |
| US 5513101 A | Radiological image interpretation apparatus and method | 19960430 | 705/3 |
| US 5498003 A | Interactive electronic games and screen savers with multiple characters | 19960312 | 463/31 |
| US 5498002 A | Interactive electronic games and screen savers with multiple characters | 19960312 | 463/31 |
| US 5495417 A | System for automatically producing different semiconductor products in different quantities through a plurality of processes along a production line | 19960227 | 700/121 |
| US 5493672 A | Concurrent simulation of host system at instruction level and input/output system at logic level with two-way communication deadlock resolution | 19960220 | 703/21 |
| US 5469353 A | Radiological image interpretation apparatus and method | 19951121 | 382/131 |
| US 5412756 A | Artificial intelligence software shell for plant operation simulation | 19950502 | 706/45 |

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| US 5404496 A | Computer-based system and method for debugging a computer system implementation | 19950404 | 714/28 |
| US 5402526 A | Interruptibility/priority control scheme for artificial intelligence software shell | 19950328 | 706/49 |
| US 5402524 A | Case-based knowledge source for artificial intelligence software shell | 19950328 | 706/45 |
| US 5398304 A | Control process for artificial intelligence software shell | 19950314 | 706/49 |
| US 5390300 A | Real time I/O operation in a vector processing computer system by running designated processors in privileged mode and bypass the operating system Method for accurate calculation of vertex movement for three-dimensional topography simulation | 19950214 | 709/103 |
| US 5377118 A | Airport surface surveillance system | 19941227 | 700/182 |
| US 5374932 A | Automatic generation of test drivers | 19941220 | 342/36 |
| US 5359546 A | Automatic generation of auto-checking testing functions | 19941025 | 702/123 |
| US 5357452 A | Lockless concurrent B-tree index meta access method for cached nodes | 19941018 | 702/108 |
| US 5283894 A | Aircraft flight emulation test system | 19940201 | 707/1 |
| US 5260874 A | Routing technique for a hierarchical interprocessor-communication network between massively-parallel processors | 19931109 | 701/33 |
| US 5224100 A | Block diagram system and method for controlling electronic instruments with simulated graphic display | 19930629 | 370/408 |
| US 5155836 A | Multiple cooperating and concurrently operating processors using individually dedicated memories | 19921013 | 703/23 |
| US 5113500 A | Computer-aided engineering | 19920512 | 710/305 |
| US 5111413 A | Method for determining the relative HAZ toughness of steel | 19920505 | 703/14 |
| US 5080732 A | Data-flow multiprocessor architecture with three dimensional multistage interconnection network for efficient signal and data processing | 19920114 | 148/508 |
| US 5021947 A | Entity-attribute value database system with inverse attribute for selectively relating two different entities | 19910604 | 712/25 |
| US 5010478 A | Method and apparatus for real-time control | 19910423 | 707/100 |
| US 4975865 A | Multicomputer digital processing system | 19901204 | 700/10 |
| US 4870704 A | System for producing graphic displays and programs for generating same | 19890926 | 710/120 |
| US 4870397 A | Block diagram editor system and method for controlling electronic instruments | 19890926 | 345/778 |
| US 4868785 A | Spark trainer for welders | 19890919 | 345/440 |
| US 4689021 A | Welder's trainer | 19870825 | 434/234 |
| US 4680014 A | Apparatus for cutting and forming tubing | 19870714 | 434/234 |
| US 4281530 A | MECHANISM FOR INSTRUCTION DATA FORWARDING IN A PIPELINE | 19810804 | 72/294 |
| US 20030154364 A1 | PROCESSOR | 20030814 | 712/218 |
| US 20030144828 A1 | Hub array system and method | 20030731 | 703/21 |
| US 20030135354 A1 | Tracking coverage results in a batch simulation farm network | 20030717 | 703/13 |
| US 20030125924 A1 | System and method for simulating computer network devices for competency training and testing simulations | 20030703 | 703/20 |

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|-------------------|--|----------|---------|
| US 20030125923 A1 | Simulation of di/dt-induced power supply voltage variation | 20030703 | 703/20 |
| US 20030125922 A1 | Mechanism for estimating and controlling di/dt-induced power supply voltage variations | 20030703 | 703/18 |
| US 20030125915 A1 | Count data access in a distributed simulation environment | 20030703 | 703/13 |
| US 20030121027 A1 | Behavioral abstractions for debugging coordination-centric software designs | 20030626 | 717/135 |
| US 20030118081 A1 | Programmable modem apparatus for transmitting and receiving digital data, design method and use method for the modem | 20030626 | 375/130 |
| US 20030115358 A1 | Unified interprocess communication | 20030619 | 709/238 |
| US 20030105620 A1 | System, method and article of manufacture for interface constructs in a programming language capable of programming hardware architectures | 20030605 | 703/22 |
| US 20030101382 A1 | Fail thresholding in a batch simulation farm network | 20030529 | 714/39 |
| US 20030101041 A1 | Annealing harvest event testcase collection within a batch simulation farm | 20030529 | 703/22 |
| US 20030101039 A1 | Maintaining data integrity within a distributed simulation environment | 20030529 | 703/16 |
| US 20030101038 A1 | Centralized disablement of instrumentation events within a batch simulation farm network | 20030529 | 703/16 |
| US 20030101035 A1 | Non-redundant collection of harvest events within a batch simulation farm network | 20030529 | 703/13 |
| US 20030095514 A1 | Communication device and communication method network system and robot apparatus | 20030522 | 370/328 |
| US 20030093187 A1 | PFN/TRAC system™ FAA upgrades for accountable remote and robotics control to stop the unauthorized use of aircraft and to improve equipment management | 20030515 | 701/1 |
| US 20030088586 A1 | User interface for a multi-dimensional data store | 20030508 | 707/200 |
| US 20030079207 A1 | Apparatus and method for interaction phenomena with world modules in data-flow-based simulation | 20030424 | 717/135 |
| US 20030074177 A1 | System, method and article of manufacture for a simulator plug-in for co-simulation purposes | 20030417 | 703/22 |
| US 20030046671 A1 | System, method and article of manufacture for signal constructs in a programming language capable of programming hardware architectures | 20030306 | 717/141 |
| US 20030046668 A1 | System, method and article of manufacture for distributing IP cores | 20030306 | 717/131 |
| US 20030040897 A1 | Man machine interface for power management control systems | 20030227 | 703/18 |
| US 20030037321 A1 | System, method and article of manufacture for extensions in a programming language capable of programming hardware architectures | 20030220 | 717/149 |
| US 20030033594 A1 | System, method and article of manufacture for parameterized expression libraries | 20030213 | 717/141 |
| US 20030033588 A1 | System, method and article of manufacture for using a library map to create and maintain IP cores effectively | 20030213 | 717/107 |
| US 20030033480 A1 | Visual program memory hierarchy optimization | 20030213 | 711/118 |
| US 20030028864 A1 | System, method and article of manufacture for successive compilations using incomplete parameters | 20030206 | 717/141 |

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|-------------------|--|----------|---------|
| US 20030028362 A1 | Bus simulation apparatus and bus simulation program | 20030206 | 703/14 |
| US 20030023958 A1 | Intermediate language accelerator chip | 20030130 | 717/146 |
| US 20030014611 A1 | Software for designing, modelling or performing digital signal processing | 20030116 | 712/35 |
| US 20030008684 A1 | Digital wireless basestation | 20030109 | 455/561 |
| US 20030004697 A1 | Method of designing, modelling or fabricating a communications baseband stack | 20030102 | 703/13 |
| US 20030001842 A1 | Object visibility control for ray tracing | 20030102 | 345/426 |
| US 20020199173 A1 | System, method and article of manufacture for a debugger capable of operating across multiple threads and lock domains | 20021226 | 717/129 |
| US 20020184348 A1 | Object oriented framework architecture for sensing and/or control environments | 20021205 | 709/220 |
| US 20020174415 A1 | System and method for debugging distributed software environments | 20021121 | 717/127 |
| US 20020169863 A1 | Multi-client to multi-server simulation environment control system (JULEP) | 20021114 | 709/223 |
| US 20020152060 A1 | Inter-chip communication system | 20021017 | 703/17 |
| US 20020143929 A1 | Method and system for collection and storage of traffic data from heterogeneous network elements in a computer network | 20021003 | 709/224 |
| US 20020143928 A1 | Method and system for collection and storage of traffic data in a computer network | 20021003 | 709/224 |
| US 20020143927 A1 | Method and system for configuring a network element in a computer network | 20021003 | 709/224 |
| US 20020143926 A1 | Method and system for collecting traffic data in a computer network | 20021003 | 709/224 |
| US 20020141351 A1 | Method and system for validating network transformation instructions | 20021003 | 370/254 |
| US 20020141342 A1 | Method and system for automatically directing data in a computer network | 20021003 | 370/235 |
| US 20020137501 A1 | Systems and methods for wireless memory programming | 20020926 | 455/419 |
| US 20020129329 A1 | Method for creating an application software model | 20020912 | 717/104 |
| US 20020107678 A1 | Virtual computer verification platform | 20020808 | 703/14 |
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| US 20020101824 A1 | System and method for connecting a logic circuit simulation to a network | 20020801 | 370/241 |
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